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硕士学位论文

宽带无线自组织网的物理层差错控制机制的研究  
与实现

Research and Implementation of the PHY  
Error Control Mechanism on Broadband  
Wireless Ad Hoc Network

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## 摘 要

无线自组织网是一组自治的无线节点相互协作而形成的独立于固定的基础设施并采用分布式管理的网络。由于它具有组网灵活、容易实现等优点，在军事通信、应急通信等方面有广阔的应用前景。宽带无线自组织网是一种无线通信和计算机网络相结合的网络，它的应用环境复杂，信道误码率高，数据传输速率高，实时性好，要求有较高的可靠性。因此相对于一般通信系统，有效的差错控制方案对于宽带无线自组织网而言显得更为重要。

CTC与Turbo码相比具有编码效率高，相同复杂度译码器下纠错性能好，译码迭代次数少，译码时延小等优点，同时消除了尾比特对译码性能的影响。文中通过仿真验证，得到译码迭代次数相同时，CTC的性能略优于Turbo码。

本文利用Matlab搭建了完整的HARQ系统仿真平台，以CTC码为前向纠错码，在COST-207 TU信道下，研究分析了不同的设计参数、不同实现方案对于系统性能的影响。综合各性能影响参数，探讨了两种HARQ自适应方案：结合星座图重构的Type-II HARQ；结合交织重组、星座图映射的Type-III HARQ (RV=1) 系统。通过仿真比较得到两个方案的性能相当，当采用QPSK调制时， $E_s/N_0=0\text{dB}$ ，误码性可达到 $10^{-3}$ ，吞吐量约为0.6。Type-III HARQ (RV=1) 系统实现相对简单，需要的缓存较少，其误码率及吞吐量性能略逊于Type-II HARQ，但差距不超过0.3dB。

最后，完成了基于OFDM的HARQ系统的FPGA设计。该系统以CTC码为前向纠错码，重传策略采用Type-III (RV=1)，码合并采用比特级的似然比合并方式，并结合交织重组与星座图重构技术。文中对各主要模块的FPGA实现进行了详细的说明，包括CRC编解码、发送缓存模块、CTC编译码、OFDM调制解调、接收缓存模块等。特别是发送缓存模块及接收缓存模块，由于涉及HARQ重传控制，接收合并及缓存管理等功能，文中根据自组织网的空中接口及模块实现的可行性对其进行了详细的阐述，通过仿真验证模块工作正常。

**关键词：**无线自组织网；CTC；HARQ；FPGA

## Abstract

Wireless Ad Hoc network is a kind of peer-to-peer network. With the advantages of network flexibility and easy realization, it has bright prospects in military communications, emergency communications, etc. Wireless Ad Hoc network incorporates wireless communications networks and computer networks.

Compared with the general communication system, more effective error control scheme is required in broadband wireless Ad Hoc networks.

Compared with Turbo code, CTC is characterized with higher coding efficiency, better error correction performance, fewer decoding iterations and lower decoding delay with the same decoder complexity. In the paper, it has been verified by simulation that CTC's performance is better than Turbo codes with the same number of decoding iterations.

In this paper, a complete HARQ system simulation platform is built by Matlab, in which CTC serves as the FEC code. Also a research is performed on the effects of different design parameters and implementation options to system performance under COST-207 TU channel. Two kinds of adaptive HARQ schemes are proposed: Type-II HARQ system which makes use of constellation rearrangement and Type-III HARQ (RV=1) system which combined with interleaving reorganization and constellation rearrangement. The performances of two schemes are similar in a BER performance of  $10^{-3}$  and throughput of about 0.6, when  $E_s/N_0=0\text{dB}$  and QPSK modulation is choosed. The implementation of Type-III HARQ (RV=1) systems is relatively simple, requiring less buffer while the bit error rate and throughput performance slightly are inferior to Type-II HARQ's, but the gap does not exceed 0.3dB.

Finally, the design of OFDM-HARQ system is completed based on FPGA with CTC as the FEC code and Type-III (RV = 1) as retransmission scheme. Moreover LLR is taken for code combination as well as the technology of interleaving

reorganization and constellation rearrangement are adopted in the final system. The FPGA implementation of the mainly modules is elaborated in the paper, including the CRC encoding and decoding, send buffer, CTC encoding and decoding, OFDM modulation and demodulation, the receive buffer module. Especially, since the combination module of send/receive buffer greatly is associated with HARQ retransmission control, a detailed analysis is carried out in this paper based on wireless interface of adhoc network and the the viability of implementation.

**Keywords:** Ad Hoc; CTC; HARQ; FPGA

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